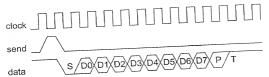


FIG. 1



S - start bit D0-D7 - data bits P - parity bit T - stop bit

FIG. 2

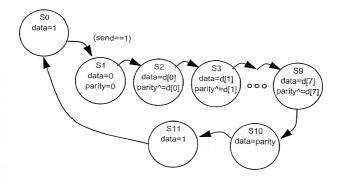


FIG. 3